Reply Sheet for Technical Inquiry

Copy:

Attent:
P海営

MELCO Power Device Works. MKT-B

<table>
<thead>
<tr>
<th>Report</th>
<th>Date</th>
<th>No.</th>
<th>CM-641</th>
<th>Customer</th>
<th>Elin</th>
</tr>
</thead>
<tbody>
<tr>
<td>Request</td>
<td>CMxx-24Aの1, 2素子の並列使用について</td>
<td></td>
<td>Person in charge</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

We'd like to report you as follows.

Question / Inquiry


並列使用についての問い合わせ

Reply:

<table>
<thead>
<tr>
<th>Check</th>
<th>Draft</th>
</tr>
</thead>
<tbody>
<tr>
<td>T. Amano 09-Mar-64</td>
<td>M. Ando</td>
</tr>
</tbody>
</table>

応用技術資料 CMH－5777－A をご参照ください。

以上
Issue: The Parallel connection of A-series IGBT (CMxx-24A)


This parallel specification is applied for above A series IGBT module.

Parallel specification
1) Shipping;
   We ship the classified modules which are in the same rank of VCE(sat) as following table.1 according to the paralleled number of modules on the orders received.

2) Indication;
   The symbol of VCE(sat) rank is marked on the module label. (for example, D or E, etc.)

Table.1: VCE(sat) rank

<table>
<thead>
<tr>
<th>symbol</th>
<th>1200V series (unit:V)</th>
</tr>
</thead>
<tbody>
<tr>
<td>D</td>
<td>1.76 ~ 1.96</td>
</tr>
<tr>
<td>E</td>
<td>1.92 ~ 2.16</td>
</tr>
<tr>
<td>F</td>
<td>2.12 ~ 2.41</td>
</tr>
<tr>
<td>G</td>
<td>2.37 ~ 2.69</td>
</tr>
<tr>
<td>H</td>
<td>2.65 ~ 3.00</td>
</tr>
</tbody>
</table>

Conditions:
Ic=rating current, VGE=+15V, Tj=25degreeC,
measurement point = between the collector terminal and gate side emitter terminal.

Notes:
1. This table is settled from the view point of keeping current unbalance within +/-15% at its rating.

2. Modules of same rank should be applied only for each paralleled connection, and it permits to use the different rank modules to the different phase or axis in one equipment.

3. This rank specification is useful for the static balance at DC current portion, and this is not effective for dynamic balance at switching transition. As the switching balance is dominated by not module but wiring inductance in the equipment, take care of the symmetric circuit design and layout for keeping the inductance balance at parallel use.
If more than 2 devices are to be paralleled, the current derating factor is determined by the formula, which we have been using to the H, U, F and NF series IGBT module.

Derating factor = \[1-\{(n-1)*(1-X)/(1+X)+1\}/n\]

n = number of devices to be paralleled
X = constant number 0.15 for 1200V rating devices

For instance, in the case of four IGBT modules of CM300DY-24A connected in parallel,

Derating factor = \[1-\{(4-1)*(1-0.15)/(1+0.15)+1\}/4\]
= \[1-(3*0.85/1.15+1)/4\]
= 0.1956
= 19.6%

So, the derated current with 4 parallel of this module is

Derated current = 300A*(1-0.196)*4
= 965A